

IrDA[®] Compliant Protocol Processor **ACT-IR8260D /ACT-IR8261D**

- ACT-IR8260D /ACT-IR8261D (or briefly, **ACT-IR826xD**) has a complete IrDA[®] Protocol stack in a single chip.
- No any driver needed.
- Includes IrPHY[™] encoding/decoding and interfaces directly to Infrared transceivers for data rate up to 115.2 kbps. Only an external Infrared transceiver is needed to complete an IrDA compliant infrared communication subsystem.
- Supports mandatory IrDA layers: IrPHY[™], IrLAP[™], IrLMP[™] and IAS[™].
- Supports upper layers: TinyTP[™], IrCOMM[™], IrLPT[™], and OBEX[™].
- Supports host baud rate from 1.2 kbps to 115.2 kbps, which is changed by PC configuration utility or hardware jumper. IrDA baud rate from 9.6 kbps to 115.2 kbps, which is flexible, setting by IrDA devices.
- Supports both IrDA Primary and Secondary modes.
- Buffer size

	Buffer (Bytes)				
	Host Inbound	Host Outbound	IrDA In Frames	IrDA Out Frames	Total
ACT-IR8261D	2K	2K	1K	1K	6K
ACT-IR8260D	1K	1K	0.5K	0.5K	3K

- Current consumption: Stand-by: 105 μ A; Active: 8.8mA.
- Small low profile plastic 64-pin QFP package.
- Available in programmed and tested chips, assembled & tested boards, or fully packaged dongles. All passed IrDA- "IrReady[™]" certification tests!



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- A ready-made IrDA[®]-compatible **evaluation dongles** ACT-IR100UD-v2 is available. Before purchasing ACT-IR826xD chip, we strongly recommend you to buy a set of **Evaluation Kit: [ACT-IR826xDEK](#)**, which is equal to ACT-IR826xDDB (**D**aughter **B**oard) plus ACT-IR826xDMB (**M**other **B**oard with USB Virtual COM port support).
- Another very useful **Evaluation Kit Full Set** is [ACT-IR100UDK-v2](#), which includes: ACT-IR100UD-v2 and ACT-IR4002US (IrDA FIR USB adapter). This kit can evaluate data transmission between ACT-IR100UD-v2 (your device) and ACT-IR4002US (PC), running under HyperTerminal in Windows. It can avoid debugging multiple issues at the same time.
e.g. :
 - Does IrDA software activate and behave properly with the matching protocol layer?
 - Is this an IR dongle to host interface issue (UART data rates, flow control, data bit/parity/stop bit, UART signal pins, power levels)?
 - Or is this a performance issues (throughput, distance, error rate/dropping bits)?

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