A complete IrDA® Protocol stack in a single chip, including mandatory protocols (IrPHY™, IrLAP™, IrLMP™) and optional protocols (IrLPT™, IrCOMM™+TinyTP™, OBEX transport or TinyTP with customized IAS™ class name for IrSocket).

Also includes IrPHY™ encoding/decoding, and interfaces directly to Infrared transceivers for data rate from 9.6kbps up to 115.2 kbps. Only an external Infrared transceiver is needed to complete an IrDA compliant infrared communication subsystem.

Supports IrDA® Secondary mode only.

Supports 64 bytes data packet for IrDA® IrLAP™ frame.

Interfaces to Host device via a full function UART port.

Supports host baud rate from 300k bps to 115.2k bps selecting by programmable settings.

Programmable Device name, IAS™ class name and data format setting.

Available in programmed and tested chip (ACT-IR8250P), assembled & tested board (ACT-IR8250PDB), or assembled & tested evaluation kit (ACT-IR8250PEK).

Low supply voltage, 3.0 V to 3.6 V.

Low current consumption; 2 μA standby, 3 mA active.

Small low profile plastic 28-pin SSOP/TSSOP package.

In-system-programmable FLASH, facilitates firmware changes or updates.

The evaluation kit (ACT-IR8250PEK) includes AC power supply, RS232 level translators, and PC software for in-system re-programming of firmware and future firmware options.

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